

ABSTRACT OF THE DISCLOSURE

In a liquid crystal display device including a light shielding film, the liquid crystal display device having a structure capable of avoiding a gate line delay is provided. In the liquid crystal display device including a thin film transistor formed on a substrate, a portion of a light shielding film for shielding against incident light from the side of the substrate, except its portion for shielding the pixel transistor, is disposed under an additional capacitance line or between a gate line and the additional capacitance line and at a position avoiding the gate line, so that parasitic capacitance between the film and the gate line is suppressed.